Appl. No. 10/711,794 Amdt. dated July 27, 2005 Reply to Office action of March 28, 2005

## **AMENDMENTS TO THE CLAIMS**

Claim 1 (currently amended): A package structure comprising:

- a lead frame having a plurality of first leads, each of which includes a first recession;
- at least a first device semiconductor chip; and
- a plurality of <u>first</u> solder joints respectively positioned in the first recessions for connecting the <u>first device</u> <u>semiconductor chip</u> to the lead frame.
- Claim 2 (currently amended) The package structure of claim 1 wherein the first device is a semiconductor chip. lead frame further comprises a plurality of second leads, each of which includes a second recession.
- Claim 3 (currently amended): The package structure of claim 12 wherein the package structure comprises at least one passive device having a plurality of outputs respectively positioned in the second recessions, and a plurality of second solder joints respectively positioned in the second recessions for connecting the second passive device to the lead frame.
- Claim 4 (original): The package structure of claim 3 wherein the passive device is an electrical resistor, a capacitor, or an inductor.

Claim 5 (cancelled)

25 Claim 6 (cancelled)

5

Claim 7 (cancelled)

Appl. No. 10/711,794 Amdt. dated July 27, 2005 Reply to Office action of March 28, 2005

## Claim 8 (cancelled)

Claim 9 (currently amended): The package structure of claim <u>83</u> wherein the seconddevice is a semiconductor die and the second solder joints is are composed comprised of tin or tin alloy.

Claim 10 (currently amended): The package structure of claim 21 wherein the lead frame comprises a die pad connected to the first device-semiconductor chip for radiating the heat produced by the first device-semiconductor chip by serving as a heat sink.

10

5

Claim 11 (currently amended): The package structure of claim 10 wherein the ehip sink die pad includes comprises a ground pad connected to both ground and the first device semiconductor chip.

Claim 12 (currently amended): The package structure of claim 1 wherein the first solder joints is are composed comprised of tin or tin alloy.

Claim 13 (new): The package structure comprising:

- a lead frame having a plurality of first leads, wherein at least two of the first leads comprises a first recession;
- at least a passive device, wherein each output of the passive device is respectively positioned in the first recession; and
  - a plurality of first solder joints respectively positioned in the first recessions for connecting the passive device to the lead frame.
- Claim 14 (new): The package structure of claim 13 wherein the package structure further comprises a semiconductor chip and a plurality of leading wires, wherein the semiconductor chip is connected to the leading wires.
  - Claim 15 (new): The package structure of claim 13 wherein the passive device is an

Appl. No. 10/711,794 Amdt. dated July 27, 2005 Reply to Office action of March 28, 2005

electrical resistor, a capacitor, or an inductor.

Claim 16 (new): The package structure of claim 13 wherein the first solder joints are comprised of tin or tin alloy.

5